



DMT32M5LPS

30V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _C = +25°C
30V	2.0mΩ @ V _{GS} = 10V	100A
300	3.0mΩ @ V _{GS} = 4.5V	100A

Description and Applications

This new generation MOSFET is designed to minimize $R_{DS(ON)}$, yet maintain superior switching performance. This device is ideal for use in power management and load switch.

- DC-DC Converters
- Load Switch

Features

- Thermally Efficient Package-Cooler Running Applications
- <1.1mm Package Profile Ideal for Thin Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

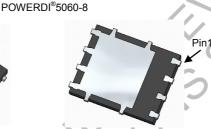
Mechanical Data

- Case: POWERDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.

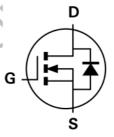
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



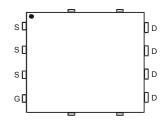




Bottom View



Internal Schematic



Top View Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging	
DMT32M5LPS-13	POWERDI [®] 5060-8	2,500 / Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



)¦¦ = Manufacturer's Marking T32M5LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 16 = 2016) WW = Week (01 to 53)

 $\label{eq:powerpower} \mbox{POWERDI is a registered trademark of Diodes Incorporated.} \\ \mbox{DMT32M5LPS}$

Document number: DSXXXXX Rev. 1 - 0



Maximum Ratings (@T_C = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current, V_{GS} = 10V (Note 6) State T_C = +25°C State T_C = +100°C		Ι _D	100 100	A	
Maximum Continuous Body Diode Forward Current (Note 6)			Is	80	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	120	Α
Avalanche Current, L=0.1mH			I _{AS}	50	А
Avalanche Energy, L=0.1mH			Eas	140	mJ

Thermal Characteristics ($@T_C = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = 25°C	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	46	°C/W
Total Power Dissipation (Note 6)	T _C = 25°C	P_{D}	100	W
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	1.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@Tc = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		-	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	1	-	3	V	$V_{DS} = V_{GS}$, $I_D = 1mA$	
Static Drain-Source On-Resistance		-	1.6	2.0	mΩ	$V_{GS} = 10V, I_D = 30A$	
Static Dialif-Source Off-Resistance	R _{DS(ON)}		2.3	3.0	11122	$V_{GS} = 4.5V$, $I_{D} = 30A$	
Diode Forward Voltage	V _{SD}	-	0.8	1.1	V	$V_{GS} = 0V, I_{S} = 30A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	4950	-		451/11/	
Output Capacitance	Coss	-	2000	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	Crss	-	205	-		1 - 1101112	
Gate Resistance	Rg	-	0.6	-	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	38.0	-			
Total Gate Charge (V _{GS} = 10V)	Qg	-	86.3	-	nC	\/ = 15\/ \ - = 20\	
Gate-Source Charge	Q _{gs}		14.0	-	IIC	$V_{DS} = 15V, I_D = 20A$	
Gate-Drain Charge	Q_{gd}		13.5	-			
Turn-On Delay Time	t _{D(ON)}	-	9.3	-			
Turn-On Rise Time	t _R	-	15.3	-		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	35.9	-	ns	$I_D = 20A, R_G = 1.6\Omega$	
Turn-Off Fall Time	t _F	-	12.0	-			
Body Diode Reverse Recovery Time	t _{RR}	-	30.4	-	ns	L = 15A di/dt = 500A/vo	
Body Diode Reverse Recovery Charge	Q_{RR}	-	52.3	-	nC	I _S = 15A, di/dt = 500A/μs	

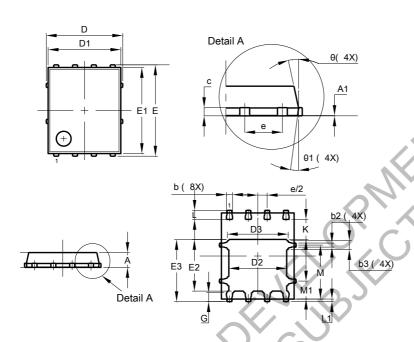
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
 7 .Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.



Package Outline Dimensions

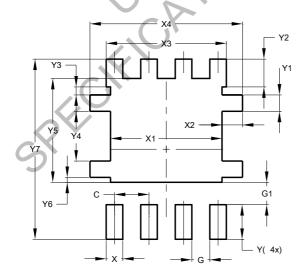
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



POWERDI [®] 5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05		
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
C	0.230	0.330	0.277	
D	,	5.15 BSC	;	
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	(6.15 BSC	;	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е	1.27 BSC			
G	0.51	0.71	0.61	
K	0.51	_	-	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
М	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
Θ	10°	12°	11°	
Θ1	6°	8°	7°	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Χ	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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